WHAT IS CLAIMED IS:

1. An electronic apparatus comprising:

a first IC which is comprising at least a first voltage input terminal, voltage limiting means for serving to limit a voltage to be applied to the first voltage input terminal to have a predetermined value, said voltage limiting means being connected electrically to the first voltage input terminal, and a first circuit block to which the voltage limited by the voltage limiting means is supplied;

an external power terminal to which a DC power voltage is applied from an outside; and

a resistor connected electrically between the external power terminal and the first voltage input terminal,

wherein the resistor and the voltage limiting means are functioning as limiting an input voltage to be applied to the first voltage input terminal to have the predetermined value when the DC power voltage to be applied to the external power terminal becomes an overvoltage.

2. The electronic apparatus according to claim 1, wherein in said electronic apparatus at least one second IC is further provided, and said at least one second IC is comprising at least a second voltage input terminal to which the input voltage is applied, and a second circuit block to which the input voltage applied to the second voltage input terminal is supplied.

3. The electronic apparatus according to claim 1, wherein said voltage limiting means is constituted by a bipolar transistor connected between the voltage input terminal and a ground, and at least one diode connected in series between a base of the bipolar transistor and an input voltage point of the voltage limiting means.

4. The electronic apparatus according to claim 1, wherein said voltage limiting means is constituted by a Zener diode

being connected between the input voltage terminal and a ground.

5. The electronic apparatus according to claim 1, wherein said voltage limiting means is constituted by a MOS transistor which is connected between the voltage input terminal and the ground, a first resistor which is connected between the gate of the MOS transistor and an input voltage point of the voltage limiting means, and a second resistor which is connected between the gate of the MOS transistor and the ground, further wherein if a gate voltage, being determined by a voltage dividing ratio of the first resistors and the second resistor, becomes more than a threshold voltage of the MOS transistor, the MOS transistor is conducted to be ON-state.